IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Arnold, et al.

Attorney Docket:

TI-36471

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

Semiconductor Chip Package

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Cite numbers A-K are newly cited prior art being filed under the waived requirement of 37 CFR 1.98 (a)(2)(i) dated July 11, 2003, therefore, copies of these U.S. patents are not included.

Respectfully submitted,

Barry W. Dove

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INFORMATION DISCLOSURE					Application Number	TBD	
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STATEMENT BY APPLICANT			Γ	First Named Inventor	Amold, et al.		
					Art Unit	TBD .	
(use as many sheets as necessary)				Examiner Name	TBD		
Sheet	1	of		1	Attorney Docket Number	TI-36471	

U.S. PATENT DOCUMENTS							
Examiner Initials*	C:1-	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where		
	Cite No.1	Number - Kind Code ^{2 (# known)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear		
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		Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,			
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OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS					
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Translation is attached.

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